

	Akhil Bharatiya Marathi Shikshan Parishad's Anantao Pawar college of Engineering & Research, Parvati, Pune		
	Record No.: ACA/R/003B	Dol: 01/02/2025	
	Revision: 00		

TE Timetable										
Academic Year 2026-27 SEM I										
W. E. F.:01/07/2026										
		Classroom No. 322								
Days	Class	9,00 AM to 10,00 AM	10,00 AM to 11,00 AM	11,00 AM to 11,15 AM	11,15 AM to 12,15 PM	12,15 AM to 1,15 AM	1,15 PM to 2,00 PM	2, 00 PM to 3,00 PM	3,00 PM to 4,00 PM	4,00 PM to 5,00 PM
Monday	T.E. (E&TC)	ML_TUT	M&A		ML_TUT T1 L-Comp TS_T2 L-DE VLSI_T3 L-VLSI		LONG BREAK	Elective_1	OE	GFM
Tuesday		M&A	VLSI		M&A_T1 L-DE Elective_L T2 L-VLSI CN_T3 L-Comp			Extra Lecture ML_TUT	Elective_1	Extra Lecture CN
Wednesday		ML_TUT	CN	SHORT BREAK	VLSI_T1 L-VLSI CN_T2 L-Comp Elective_1 T3 L-DE			Extra Lecture_OE	Elective_1	Extra lecture_VLSI
Thursday		OE	VLSI		TS_T1 L-Comp VLSI_T2 L-VLSI M&A_T3 L-DE			CN	Extra Lecture Elective_1	M&A
Friday		CN	VLSI		CN_T1 L-Comp M&A_T2 L-DE ML_T3 L-VLSI			Extra Lecture M&A	Elective_1_T1 L-VLSI ML_TUT_T2 L-Comp TS_T3 L-DE	

Class : TE	
Name of Faculty	Subject
Prof. Vaishali Bhimite	VLSI Design
Prof. Shridevi Kumbhare	Cellular Network
Prof. Priyanka Patange	Elective - I (Computer Network)
Prof. Ashwini Suryawanshi	Microcontroller and Applications
Prof. Anil Naikwade	Technical Seminar
Prof. Suhas Dake	Open Elective(OE)
New Faculty	Machine Learning

* L-LAB
PR-Project Lab(308)
PE-Power Electronics Lab(321)
Comp- Computer lab(325)
DE- Digital Electronics Lab(326)
Comm-Communication Lab(320)
VLSI-VLSI Lab(310)


 25/06/2026
 Prof. V. V. Bhimite
 TIME TABLE COORDINATOR


 25/6/26
 Dr. A. B. Deshmukh
 Head E&TC


 Dr. S. B. THAKARE
 PRINCIPAL