

	Akhil Bharatiya Maratha Shikshan Parishad's Anantrao Pawar college of Engineering & Research, Parvati, Pune		
	Record No.: ACA/R/003B	Dof: 01/02/2025	
	Revision: 00		

BE Timetable
Academic Year 2026-27 SEM I
W. E. F.:01 /07/2026

Days	Class	9.00 AM to 10.00 AM	10.00AM to 11.00 AM	11.00 AM to 11.15 AM	11.15 AM to 12.15 PM	12.15 AM to 1.15 AM	1.15 PM to 2.00 PM	2. 00 PM to 3.00 PM	3.00 PM to 4.00 PM	4.00 PM to 5.00 PM
Monday	BE (E&TC)	CC_B1 L-Comp Elective-III_B2 L-DE RMT_B3 L-Comm		SHORT BREAK	Elective-III	CC	LONG BREAK	VLSI	RMT	Extra Lecture CC
Tuesday		Elective-III_B1 L-DE CC_B2 L-Comp VLSI_B3 L-VLSI			Audit Course	Elective-III		RMT	Extra Lecture_Elective III	Library Hour
Wednesday		RMT_B1 L-Comm VLSI_B2 L-VLSI CC_B3 L-Comp			CC	Elective-IV		VLSI	Extra Lecture_Elective IV	GFM
Thursday		VLSI_B1 L-VLSI RMT_B2 L-Comm Elective-III_B3 L-DE			ElectiveIII	RMT		Elective-IV	VLSI	Project Stage-I L-PR
Friday		CC	Elective-IV		Project Stage-I L-PR			Project Stage-I L-PR	Adjunt Faculty Session	

Class :BE	
Name of Faculty	Subject
Prof. Anil Naikwade	VLSI Design& Technology(VLSI)
Dr. Amar Deshmukh	Cloud Computing(CC)
Prof. Priyanka Patange	Radiation & Microwave Techniques(RMT)
Prof. Sharad Jagtap	Elective III (JAVA Script)(JS)
Prof. Ashwini Suryawanshi	Elective IV (Electronic Product Development)(EPD)
All Faculty	Project Stage I(PS-I)
Dr. Padmakar Kelkar	Adjunt Faculty Session
Prof. Anil Naikwade	Library Hour

* L-LAB
PR-Project Lab(308)
PE-Power Electronics Lab(321)
Comp- Computer lab(325)
DE- Digital Electronics Lab(326)
Comm-Communication Lab(320)
VLSI-VLSI Lab(310)

25/06/2026
Prof. V. V. Bhimte
TIME TABLE COORDINATOR

29/6/2026
Dr. C. B. Deshmukh
Head E&TC

Dr. S. B. THAKARE
PRINCIPAL