
	Akhil Bharatiya Maratha Shikshan Parishad's Anantrao Pawar College of Engineering & Research		
	Record No.: ADM/D/036A Revision: 00	DoI: 01/02/2025	
Internal Correspondence			

Ref. APCOER/OFFICE/ /2025-26

Date: 12-Sept.-2025

NOTICE FOR ACAP / IL ADMISSION ROUND-II

A) First Year Undergraduate Courses in Engineering ACAP / IL Admission Round-II schedule

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|--------------------------------|------------------|----------------|
| 1. Reporting Time to Institute | Date- 13/09/2025 | Time: 10.00 am |
| 2. ACAP / IL Round start | Date- 13/09/2025 | Time: 11.0 am |

B) Important Instructions for Candidate & Parents:

1) The parents & candidate are informed to go through the admission schedule & Fees required to be paid by them and accordingly accept the seat. If the parents / candidates fail to pay the Fees after acceptance of seat then the said seat allotted to him/her will be cancelled and offered to next candidate in the Merit List.



2) It is compulsory to submit the necessary original documents & payment of Fees for confirmation of admission. The parents are informed to keep the Fees amount ready for online payment or by Cash. No Admission will be confirmed without payment of full Fees if seat is offered to them.

If the candidate has taken admission to other College then the online confirmation of admission letter is to be submitted against submission of original documents.

3) The Fees displayed below is to be paid through Online / Cash.

Branch	Institute Level (IL) Tuition, Development & Other Fees			
	First Year	Second Year	Third Year	Forth Year (BE)
Computer Engineering	Rs. 240511/-	Rs. 81511/-	Rs. 81511/-	Rs. 81511/-
Information Technology				
AI & DS				
E & TC	Rs. 84511/-	Rs. 81511/-	Rs. 81511/-	Rs. 81511/-

Branch	Against CAP (ACAP) seats Tuition, Development & Other Fees			
	First Year	Second Year	Third Year	Forth Year (BE)
Computer Engineering	Rs. 84511/-	Rs. 81511/-	Rs. 81511/-	Rs. 81511/-
Information Technology				
AI&DS				
Mechanical Engineering				
Civil Engineering				
E & TC Engineering				
Electronics Engg. (VLSI D&T)				
Electronics & Communication (ACT)				

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4) The Candidate should be compulsorily present for counselling round at the time of admission. All should Note that only one parent and candidate will be allowed for admission round.

5) If the candidate fails to report for admission as per the schedule given for his/her Merit Number he/she will not have a claim on the seat. If he/she fails to pay fees and complete process on time then will have no claim on any seat.

6) Admission procedure will be carried out as per rules and regulations mentioned in Maharashtra Government - CET cell Admission Boucher for A.Y. 2025-26

Admission Coordinator
(First Year Engineering)

Dr. S. B. Thakare
Principal, APCOER