

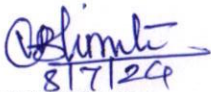
	Akhil Bharatiya Maratha Shikshan Parishad's Anantrao Pawar college of Engineering & Research, Parvati, Pune		
	Record No.: ACA/R/003B		
	Revision: 00		
		SE Timetable Academic Year 2024-25 SEM I Classroom No. 324 W. E. F.: 8/07/2024	DoI: 02/01/2023 Version: 3A.0

Days	Class	9.00 AM to 10.00 AM	10.00AM to 11.00 AM	11.00 AM to 11.15 AM	11.15 AM to 12.15 PM	12.15 AM to 1.15 AM	1.15 PM to 2.00 PM	2. 00 PM to 3.00 PM	3.00 PM to 4.00 PM
Monday	S.E. (E&TC)	DS	ECT	SHORT BREAK	EM-III	AC	LONG BREAK	EC LAB S1 BATCH (L-PR) DCT LAB S2 BATCH (L-DE) ECT LAB S3 BATCH(L-Comp)	
Tuesday		EC	DCT		ECT	EM-III		DCT LAB S1 BATCH (L-DE) ECT LAB S2 BATCH (L-PR) DS LAB S3 BATCH (L-Comp)	
Wednesday		EM-III	DS		EC	DCT		ECT LAB S1 BATCH (L-PR) DS LAB S2 BATCH (L-Comp) ESD LAB S3 BATCH (L-PE)	
Thursday		DCT	EM-III		EC	EXTRA SLOT		DS LAB S1 BATCH (L-Comp) ESD LAB S2 BATCH (L-PE) EC LAB S3 BATCH (L-PR)	
Friday		ECT	DS		EM-III_TUT	EXTRA SLOT		ESD LAB S1 BATCH (L-PE) EC LAB S2 BATCH (L-PR) DCT LAB S3 BATCH (L-DE)	
Saturday		DCT	ECT		DS	EC		EXTRA SLOT	GFM MEETING/EXTRA SLOT

Class :SE SEM-I	
Name of Faculty	Subject
Prof. Rajesh Kharat	Engineering Mathematics III(EM-III)
Prof. Prachi Upasani	Electronic Circuits(EC)
Prof. Nikita Bhagat	Digital Circuits(DCT)
Prof. Anil Naikwade	Electrical Circuits(ECT)
Prof. Ashwini Suryawanshi	Data structures(DS)
Prof. Kishor Jadhav	Electronic Skill Development(ESD)
Prof. Prachi Upasani	Mandatory Audit Course 3(AC)

* L-LAB
PR-Project Lab(308)
PE-Power Electronics Lab(321)
Comp- Computer lab(325)
DE- Digital Electronics Lab(326)

Technical English for Engineers


 Prof. V. V. Bhimte
 TIME TABLE COORDINATOR


 Dr. A. B. Deshmukh
 Head E&TC


 Dr. S. B. THAKARE
 PRINCIPAL