



52198272005

**Savitribai Phule Pune University****Teacher Profile Verification Application****For Office Use Only**

Batch No.

Sr. No.

Index

No. of Changes :

| Sr. No. | Approval Document List | | Dept. Verification | IT Verification | Document Upload Status |
|---------|-----------------------------|--------------------|--------------------------|-----------------|------------------------|
| 1 | Record ID : | CRA170824165758903 | | | |
| | Approval Ltr. No. | CCO/1087 | Date : 19/06/2017 | | |
| | Appointment Ltr. No. | | Date : | | |
| | Relieving Ltr. No. | | Date : | | |
| | PayScale Ltr. No. | | Date : | | |
| 2 | Record ID : | CRA150614104829537 | | | |
| | Approval Ltr. No. | CCO/803 | Date : 16/04/2015 | | |
| | Appointment Ltr. No. | | Date : | | |
| | Relieving Ltr. No. | | Date : | | |
| | PayScale Ltr. No. | | Date : | | |
| 3 | Record ID : | CRA121028211121583 | | | |
| | Approval Ltr. No. | CCO/1254 | Date : 12/03/2012 | | |
| | Appointment Ltr. No. | | Date : | | |
| | Relieving Ltr. No. | | Date : | | |

Signature of Teacher



52198272005



Savitribai Phule Pune University

Teacher Profile Verification Application

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|----------------|---|---------------------------|------------------------|-------------------------------|--|
| | PayScale Ltr. No. | Date : | | | |
| Sr. No. | Recognition Document List | Dept. Verification | IT Verification | Document Upload Status | |
| Sr. No. | Finance Document List | Dept. Verification | IT Verification | Document Upload Status | |
| 1 | Self Attested copy of PAN Card | | | | |
| 2 | Self Attested copy of Bank Passbook or Cancelled cheque leaf | | | | |

Signature of Teacher



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Savitribai Phule Pune University

Teacher Profile Verification Application

Part -I

| Sr. No. | 1. Personal Information | | | Verification |
|----------------|--------------------------------|--|--|---------------------|
| A1 | Full Name : | Gandhi Swati Ajay | Gender : Female | |
| A2 | Date of Birth : | 25-11-1982 | | |
| A3 | Category of Applicant : | OPEN | Designation : Assistant Professor | |
| A4 | College : | Akhil Bharatiya Maratha Shikshan Parishad Pune Anantrao Pawar College of Engineering & Research Addr: S No 103 Shahu College Campus Parvati Pune 09 Ta: Pune (corporation Area) Dist: Pune | PunCode : CEGP019670 | |
| A5 | Personal Email : | swati.gandhi25@gmail.com | Personal Mob No. : 9822809996 | |

Signature of Teacher



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Savitribai Phule Pune University

Teacher Profile Verification Application

| 2. Academic Qualification | | | | | | | |
|---------------------------|--------------|-------------|--------|---|---------------------------------------|-------------------------|--------------|
| Sr.No. | Type | Faculty | Degree | Subject Name | University Name | Month & Year of Passing | Verification |
| 1 | Graduate | Engineering | B.E. | ELECTRONICS AND TELECOMMUNICATION ENGINEERING | NORTH MAHARASHTRA UNIVERSITY, JALGAON | June-2004 | |
| 2 | PostGraduate | Engineering | M.E. | ETC | University of Pune | October-2013 | |

Personal information and Academic qualification is true according to College Record/Service Book Record.

Signature of Principal

Signature of Teacher



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**Savitribai Phule Pune University****Teacher Profile Verification Application****Part –II****3. Teaching Experience (Only having University Approval)**

| Sr No. | Designation | University | Subjects | Approval? | Nature of Appointment | Total Experience(in Months) | Verification |
|---------------|---|--|---|--|---|------------------------------------|---------------------|
| 1 | Designation : Assistant Professor , PayScale : PayScale Start Date : , PayScale Letter Date : | University Name : Savitribai Phule Pune University, Pune College Name : TSSMs PVPIT Pune | Type Of Approval (UG/PG) : PG , Faculty : Engineering , Board Of Studies : Electronics & Telecommunication , Subject/Department : , Course Name : | Approval Type : Permanent Approval Date : 19/06/2017 Approval Ref No : CCO/1087 Approval Date : 02/11/2016 To 25/10/2023 | Nature : Full Time From Date : 11/2/2016 To Date : 10/25/2023 10:29:51 AM Relieving Date : | 83 | |
| 2 | Designation : Assistant Professor , PayScale : PayScale Start Date : , PayScale Letter Date : | University Name : Savitribai Phule Pune University, Pune College Name : TSSM's PVPIT, Pune | Type Of Approval (UG/PG) : UG , Faculty : Engineering , Board Of Studies : Electronics & Telecommunication , Subject/Department : , Course Name : | Approval Type : Temporary Approval Date : 16/04/2015 Approval Ref No : CCO/803 Approval Date : 30/12/2014 To 31/05/2015 | Nature : Full Time From Date : 12/30/2014 To Date : 5/31/2015 Relieving Date : | 5 | |

Signature of Teacher



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**Savitribai Phule Pune University****Teacher Profile Verification Application**

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|---------------------------|--|---|--|--|--|------------|--|
| 3 | Designation : Assistant Professor , PayScale : PayScale Start Date : , PayScale Letter Date : | University Name : College Name : | Type Of Approval (UG/PG) : UG , Faculty : Engineering , Board Of Studies : Electronics Engineering , Subject/Department : , Course Name : | Approval Type : Temporary Approval Date : 12/03/2012 Approval Ref No : CCO/1254 Approval Date : 01/08/2011 To 29/12/2014 | Nature : Full Time From Date : 8/1/2011 To Date : 12/29/2014 Relieving Date : | 40 | |
| Total Experience : | | | | | | 128 | |

Recognition Details Not Available

Part –III**5. Teaching Papers In Last 5 Years**

| Sr.No. | Paper Name | Faculty | Course Name | Pattern Name | Academic Year | is Verified By University |
|--------|------------------|--|---|---|---------------|---------------------------|
| 1 | Computer Network | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2008 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2008 PAT) (ELECTRONICS & TELECOMMU.) | 2012-2013 | |
| 2 | Computer Network | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2008 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2008 PAT) (ELECTRONICS & TELECOMMU.) | 2013-2014 | |

Signature of Teacher



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**Savitribai Phule Pune University****Teacher Profile Verification Application**

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|----|---------------------------------|--|---|--|-----------|--|
| 3 | Embedded Processors | SCIENCE AND TECHNOLOGY : B)ENGINEERING | T.E.(2012 PAT.) (ELECTRONICS &TELECOM) | T.E.(2012 PAT.) (ELECTRONICS &TELECOM) | 2014-2015 | |
| 4 | Computer Networks | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2012 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2012 PAT) (ELECTRONICS & TELECOMMU.) | 2014-2015 | |
| 5 | Embedded Processors | SCIENCE AND TECHNOLOGY : B)ENGINEERING | T.E.(2012 PAT.) (ELECTRONICS &TELECOM) | T.E.(2012 PAT.) (ELECTRONICS &TELECOM) | 2015-2016 | |
| 6 | Computer Networks | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2012 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2012 PAT) (ELECTRONICS & TELECOMMU.) | 2015-2016 | |
| 7 | Computer Networks | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2012 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2012 PAT) (ELECTRONICS & TELECOMMU.) | 2016-2017 | |
| 8 | Wireless Networks : ELECTIVE-IV | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2012 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2012 PAT) (ELECTRONICS & TELECOMMU.) | 2016-2017 | |
| 9 | ELECTIVE - III | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATI ON (VLSI & EMBEDDED SYSTEMS) (2013 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2013 Pat.) | 2017-2018 | |
| 10 | COMPUTER NETWORKS & SECURITY | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2015 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2015 PAT) (ELECTRONICS & TELECOMMU.) | 2018-2019 | |
| 11 | ELECTIVE - I | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATI ON (VLSI & EMBEDDED SYSTEMS) (2013 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2013 Pat.) | 2018-2019 | |

Signature of Teacher



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|----|--|---|--|--|-----------|--|
| 12 | EMBEDDED SYSTEM DESIGN | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2018-2019 | |
| 13 | ELECTIVE - III | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2013 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2013 Pat.) | 2018-2019 | |
| 14 | * ELECTRICAL AND ELECTRONICS ENGINEERING | SCIENCE AND TECHNOLOGY : B)ENGINEERING | S.E.(2015 PAT.) (MECHANICAL) | S.E.(2015 PAT.) (MECHANICAL) | 2019-2020 | |
| 15 | COMPUTER NETWORKS & SECURITY | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2015 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2015 PAT) (ELECTRONICS & TELECOMMU.) | 2019-2020 | |
| 16 | EMBEDDED SYSTEM DESIGN | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2019-2020 | |
| 17 | COMPUTER NETWORKS & SECURITY | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2015 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2015 PAT) (ELECTRONICS & TELECOMMU.) | 2020-2021 | |
| 18 | EMBEDDED SYSTEM DESIGN | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2020-2021 | |
| 19 | \$ ELECTIVE - I | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2020-2021 | |

Signature of Teacher



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**Savitribai Phule Pune University****Teacher Profile Verification Application**

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|----|-------------------------------------|--|---|---|-----------|--|
| 20 | COMPUTER NETWORKS (ELECTIVE - I) | SCIENCE AND TECHNOLOGY : B)ENGINEERING | T.E.(2019 PAT.) (ELECTRONICS & TELECOM) | T.E.(2019 PAT.) (ELECTRONICS & TELECOM) | 2021-2022 | |
| 21 | COMPUTER NETWORKS & SECURITY | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2015 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2015 PAT) (ELECTRONICS & TELECOMMU.) | 2021-2022 | |
| 22 | EMBEDDED SYSTEM DESIGN | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATI ON (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2021-2022 | |
| 23 | \$ ELECTIVE - I | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATI ON (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2021-2022 | |
| 24 | EMBEDDED AUTOMOTIVE SYSTEMS | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATI ON (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2021-2022 | |
| 25 | COMPUTER NETWORKS (ELECTIVE - I) | SCIENCE AND TECHNOLOGY : B)ENGINEERING | T.E.(2019 PAT.) (ELECTRONICS & TELECOM) | T.E.(2019 PAT.) (ELECTRONICS & TELECOM) | 2022-2023 | |
| 26 | (ELECTIVE - III) MODERNIZED IOT | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2019 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2019 PAT) (ELECTRONICS & TELECOMMU.) | 2022-2023 | |
| 27 | EMBEDDED SYSTEM DESIGN | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATI ON (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2022-2023 | |

Signature of Teacher



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|----|---------------------------------|---|--|--|-----------|--|
| 28 | \$ ELECTIVE - I | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2022-2023 | |
| 29 | EMBEDDED AUTOMOTIVE SYSTEMS | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2022-2023 | |
| 30 | (ELECTIVE-I) COMPUTER NETWORKS | SCIENCE AND TECHNOLOGY : B)ENGINEERING | T.E.(2019 PAT.) (ELECTRONICS & TELECOM) | T.E.(2019 PAT.) (ELECTRONICS & TELECOM) | 2023-2024 | |
| 31 | (ELECTIVE - III) MODERNIZED IOT | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2019 PAT) (ELECTRONICS & TELECOMMU.) | B.E.(2019 PAT) (ELECTRONICS & TELECOMMU.) | 2023-2024 | |
| 32 | MACHINE LEARNING | SCIENCE AND TECHNOLOGY : B)ENGINEERING | B.E.(2019 PAT.) (HONORS/MINORS) | B.E.(2019 PAT.) (HONORS/MINORS) | 2023-2024 | |
| 33 | EMBEDDED SYSTEM DESIGN | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2023-2024 | |
| 34 | \$ ELECTIVE - I | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2023-2024 | |
| 35 | EMBEDDED AUTOMOTIVE SYSTEMS | SCIENCE AND TECHNOLOGY : B)ENGINEERING | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | M.E. ELECTRONICS & TELECOMMUNICATION (VLSI & EMBEDDED SYSTEMS) (2017 Pat.) | 2023-2024 | |

Signature of Teacher



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Teacher Profile Verification Application

Papers taught by the teacher in last five academic years are verified from Time Table/College Record.

Signature of Principal

Signature of Teacher



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Savitribai Phule Pune University

Teacher Profile Verification Application

Part –IV

| 6. Finance Details | |
|---------------------------|--|
| 1 | Pan Card No : AQZPG1146N |
| 2 | Account No : 4508000400011618 |
| 3 | Name On Account : Mrs. Swati Ajay Gandhi |
| 4 | Bank Name : Panjab National Bank |
| 5 | Branch Address : SATARA ROAD Shop No.124,Ruturang,SNo.49/1,aranyeshwar,pune37 |
| 6 | IFSC Code : PUNB0398400 |
| 7 | MICR Code : 411024013 |
| 8 | Account Type : Savings |
| 9 | Account Handling Type : Single |

Signature of Principal/Director/Chairman

Signature of Teacher